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产品型号:	PV0355B7Z0	

产品规格: 320 RGB * 480 TFT 液晶显示屏

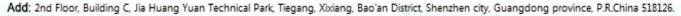
送 承 方:

制订	审 査	核准	公司印章
7 to		7 to	
2019.02,21		2019.02.21	

承 认 方:

研发部	工程部	品质部	核准	公司印章

临时规格书





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Document Revision History

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V00	2019-02-21	-	First issue	王加林

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1. LCM Specification

1.1 Description

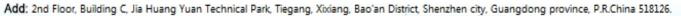
PV0355B7Z0 is a transmissive type color active matrix liquid crystal display(LCD) which uses amorphous thin film transistor(TFT) as switching devices. This product is composed of a TFT LCD panel, a drive IC, a FPC and a LED-backlight unit. The active display area is 3.5 inches diagonally measured and the native resolution is 320*RGB*480.Features of this product are listed in the following table.

1.2 Functions & Features

Table 1.1 Module Functions & Features

rabio il modalo i anotiono a i oataro								
Parameter	Value	Unit						
LCD Mode	TFT/Transmissive	-						
Color Depth	262 K color	-						
Display Resolution	320RGB*480	pixels						
Module Size	54.46(W)*82.94(H) *3.65(T)(Exclude FPC)	mm						
Active Area (A.A)	48.96(W)*73.44(H)	mm						
Pixel Arrangement	RGB-stripe	-						
Viewing Direction	ALL							
Display Mode	Normally Black							
LCD Controller/Driver	IL19488	-						
IC Package Type	COG	-						
Interface	16-Bit MCU	-						
Power Supply Voltage	2.8~3.3	V						
LCM Brightness	1000	cd /m²						
Back-light	White LED*10	PCS						

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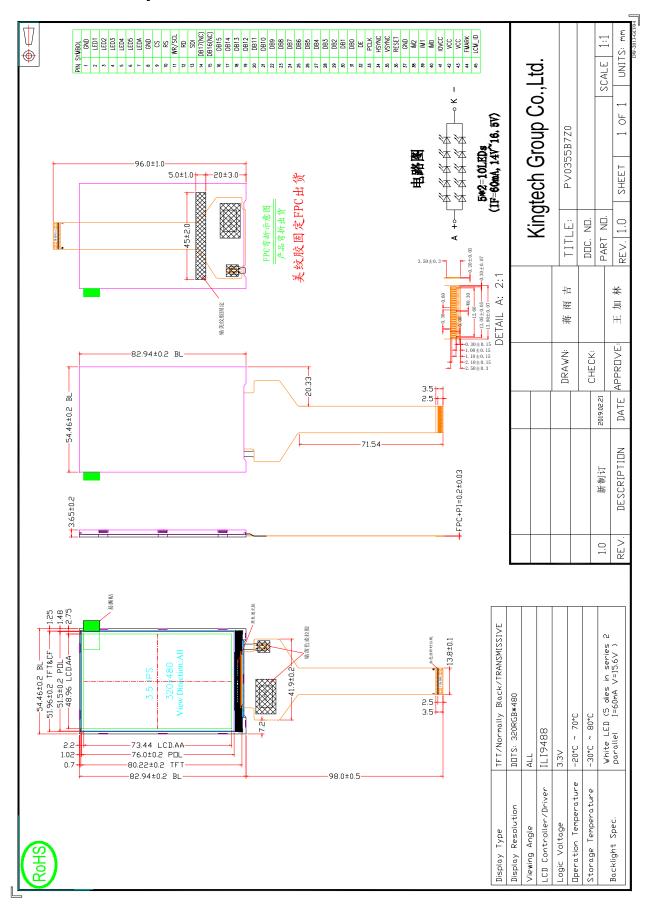
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2. Mechanical Specification



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3. Pin Descriptions

3.2 LCM PIN Descriptions

Pin No.	Symbol	1/0	Functional	Remark
1	GND	Р	System ground.	
2~6	LEDK	Р	LED Power supply -	
7	LEDA	Р	LED Power supply +	
8	GND	Р	System ground.	
9	CS	I	Chip select input pin	
10	RS	I	Selects register.	
9	WR	I	bus interface operation.	
12	RD	I	bus interface operation.	
13	SDI	I/O	Serial data in / out	
14~15	DB17~DB16	I/O	GND level when not in use.	
16~31	DB15~DB0	I/O	16-bit interface: DB0-DB15 are used	
32	DE		GND level when not in use.	
33	PCLK	Р	IOVCC level when not in use.	
34	HSYNC		GND level when not in use.	
35	VSYNC	1	GND level when not in use.	
36	RESET		RESX pin	
37	GND	Р	System ground.	
38~40	IM[2:0]	0	Select the interface mode. IM[2:0]=010B	
41	IOVCC	Р	Power supply, 1.65~3.2V	
42~43	VCC	Р	Power supply, 2.5~3.2V	
44	FMARK	0	-	
45	LCM_ID	0	LCM ID	

3.2 TP PIN Descriptions

TBD.

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4. Electrical Units

4.1 Absolute Maximum Ratings

The absolute maximum ratings are list on Table 4.1. When used out of the absolute maximum ratings, the LCM may be permanently damaged. Using the LCM within the following electrical characteristics limit is strongly recommended for normal operation. If these electrical characteristic conditions are exceeded during normal operation, the LCM will malfunction and cause poor reliability.

Table 4.1 Module Absolute Maximum Ratings

Item	Symbol	Unit	Value	Note
Input power supply	Vdd	V	-0.3 to +4.6	
Analog power supply	Avdd	Avdd V -0.3 to -		
Operating Temperature	Тор	°C	-20 to +70	
Storage Temperature	Tst	°C	-30 to +80	
Operating Humidity	Нор	%(RH)	90	

(VSS=0V)

4.2 Electrical characteristics

Table 4.2:DC Characteristic

Ite	m	Symbol	nbol Condition Min. Type. Max.		Max.	Unit	
Supply Voltage	Logic	IOVCC		1.65	1.8	3.2	V
Analog power supply	Logic	VCC		2.5	2.8	3.2	V
Input	H level	ViH		0.7IOVdd		IOVdd	
Voltage	L level	VIL	-	DGND		0.3IOVdd	V
Output	H level	V _o н		0.8IOVdd		lOvdd	V
Voltage	L level	Vol		DGND		0.2IOVdd	V

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4.3 Back-light Specification

Table 4.3 Back-light Characteristics

Tuble 4.6 Back light Characteristics							
Item	Symbol	Conditi	ions	Min.	Type.	Max.	Unit
Supply Voltage	VF	Only Backlid	ıht	14.0	15.0	17.0	V
Supply Current	IF	Only Backlight		60			mA
Average Brightness	IV	Backlight Cu IF=120mA	ırrent	12000			Cd/ m ²
CIE Color Coordinate	Х	Backlight	Current	TBD		TBD	_
(Without LCD)	Y	IF=120mA		TBD	1	TBD	
Uniformity	В	Backlight IF=120mA	Current		80%	_	%
Color		•	Whi	te			

Note: 5 LEDs in Series 2 Parallel connection.

4.4 TP Specification

TBD.

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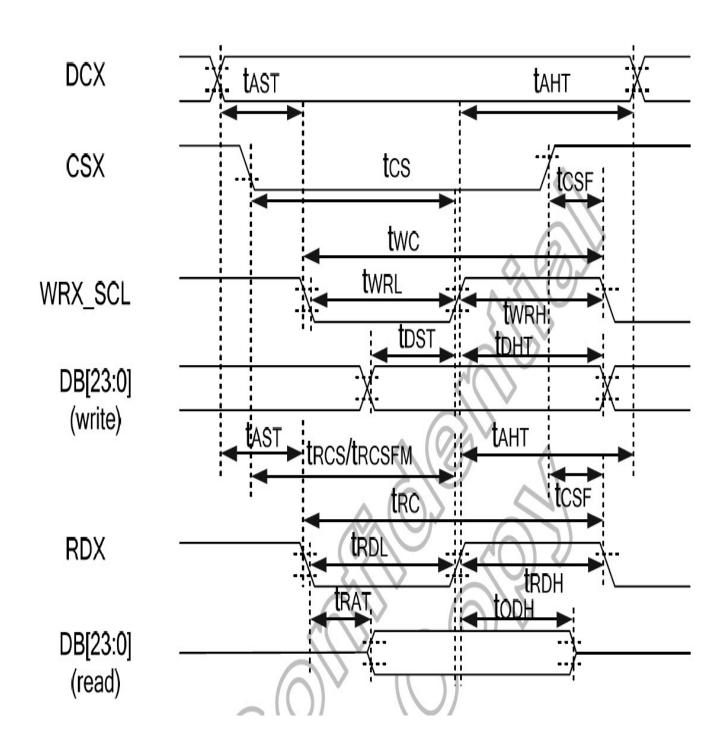
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5. AC Characteristics

5.1 16-Bit MCU Interface



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		* (VSSA=0V, IOVCC=1.8V, V	VCI=2.8	V, TA=2	5°C)	
Signal	Symbol	Parameter	Min.	Max.	Unit	Description
DCX	Tast	Address setup time	0	-	na	
DCY	T - 1- +	A 1 1 1 1 1 1 1 1	10		ns	

Digital	Dymoor	I GI GIIC CCI	1/11111	max.	OHILU	Description
DCX	Tast	Address setup time	0	_	ng	
DCX	Taht	Address hold time (Write/Read)	10	_	ns	
	Tcs	Chip select setup time (Write)	10	_		
CCV	Trcs	Chip select setup time (Read register)	45	_		
CSX	Trcsfm	Chip select setup time (GRAM)	355	_	ns	
	Tcsf	Chip select wait time (Write/Read) 10 -				
	Twc	Write cycle (write register)	50	_		
	Twc	Write cycle (write GRAM@SLPOUT)	47	_		
WRX	Twc	Write cycle (write GRAM@SLPIN)	100	_	ns	
	Twrh	Control pulse "H" duration	15	_		
	Twr1	Control pulse "L" duration 15 -				
	Trc	Read cycle (read register)	160	_		
	Trc	Read cycle (GRAM)	450	_		
RDX	Trdh	Control pulse "H" duration	90	_	ns	
	Trdl	Control pulse "L" duration(read register)	35	_		
	Trdl	Control pulse "L" duration(GRAM)	345	_		
	Tdst	Data setup time	10	_		
	Tdht	Data hold time	10	_		For maximum
DB[23:0]	Trat	Read access time(read register)	_	40	ns	CL=30pF For minimum
	Trat	Read access time(GRAM)	_	340		CL=8pF
	Todh	Output disable time	20	80		

Note: The input signal rise time and fall time (tr, tf) is specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of IOVCC for Input signals.

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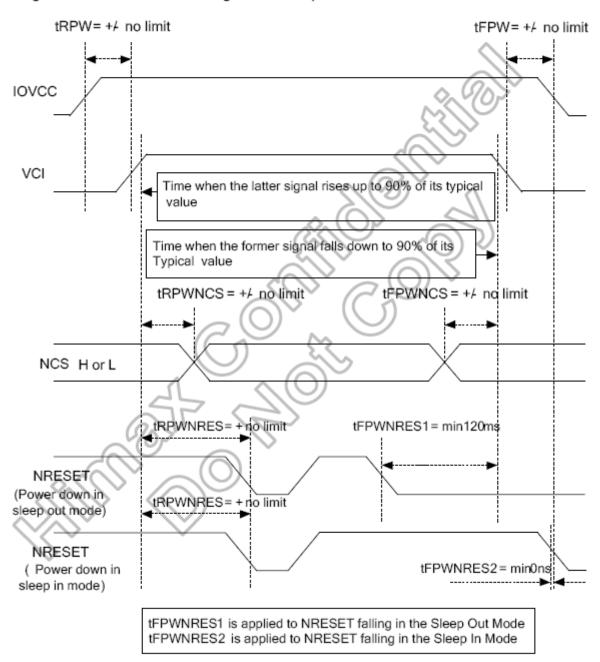
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6. Power On/Off Sequence

6.1 Case 1 – RESX line is held high or Unstable by Host at Power –On

If NRESET line is held high or unstable by the host during Power On, then a Hardware Reset must be applied after both IOVCC, VCI have been applied, otherwise correct functionality is not guaranteed. There is no timing restriction upon this hardware reset.



Note: Unless otherwise specified timings herein show cross point at 50% of signal/power level Figure 5.31: Case 1 – NRESET line is held high or unstable by host at power on

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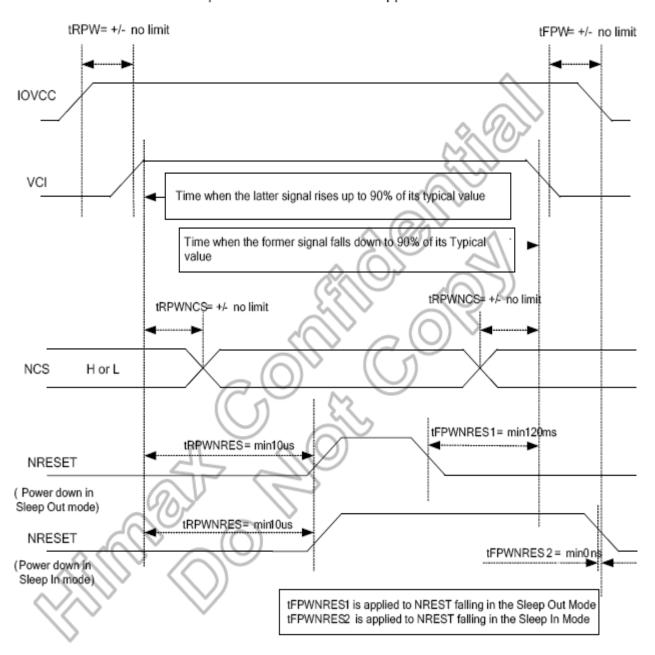
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6.2 Case 2 – RESX line is held Low by Host at Power On

If NRESET line is held Low (and stable) by the host during Power On, then the NRESET must be held low for minimum 10µsec after VCI have been applied.



Note: Unless otherwise specified timings herein show cross point at 50% of signal/power level

Figure 5.32: NRESET line is held low by host at power on

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7. Optical Specification

7.1 Optical Specification

Light Source: C-light

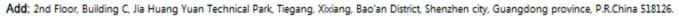
(With UP Polarizer: APCFH4CVT, Down Polarizer: NPFCVT1764FCUARC9) Ta=25℃

ltem		Symbol	Condition	Min	Тур	Max	Unit	Remark
NG A 1		θТ		80	-	-		
		θВ	CD > 10	80	-	-	_	Nata 0
View Angle		θL	CR≧10	80	-	-	Degree	Note 2
		θR		80	-	-, 1		
Contrast Ratio		CR	θ=0°	400	800	_	Y	Note1 Note3
Response Tim		Ton	25℃	-	25	35	ms	Note1
response rim		Toff	θ=0°					Note4
	White	х		0.27	0.31	0.35		
	vviille	у		0.28	0.32	0.36		
	Red	х		1	-	-		
Color Filter	Red	у	θ=0°		/ -	-		
Chromaticity	Green -	х	0-0	-	-	-		
		у			-	-		
	Blue	х		-	-	-		
	Diue	У		-	-	-		
NTSC		-	θ=0°	-	72.8	-	%	Note 5
Transmittance) ^T	θ=0°	-	4.4	-	%	Note1 Note5

Test Conditions:

- The ambient temperature is 25℃.
- The test systems refer to Note 1 and Note 2.
- The Transmittance and NTSC are the emulated values base on the panel with normal polarizer and C-Light, and when using LED back light they will be to decrease about 0.3%.

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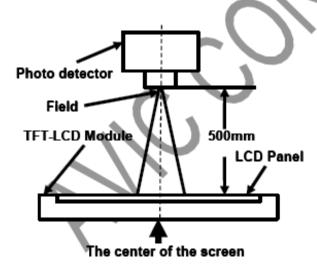
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Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Item	Photo detector	Field
Contrast Ratio	SR-3A	1°
Chromaticity	011 071	·
Response Time	BM-7A	2°

Note 2: Definition of viewing angle range and measurement system, viewing angle is measured at the center point of the LCD by CONOSCOPE (ergo-80).

Viewing angle is measured With EWV Polarizer.

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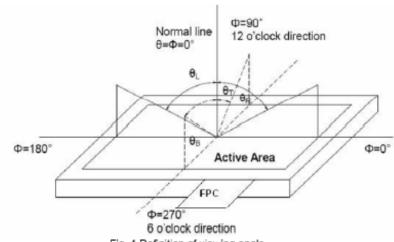


Fig. 1 Definition of viewing angle

Note 3: Definition of contrast ratio

Contrast ratio (CR) = $\frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$

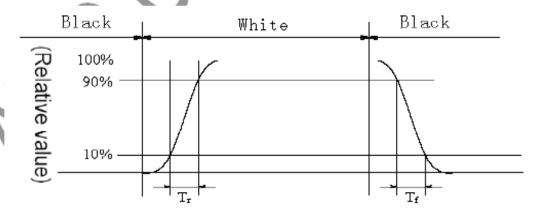
"White state ": The state is that the LCD should driven by Vwhite

"Black state": The state is that the LCD should driven by Vblack.

Vwhite: To be determined Vblack: To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

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8. Reliability Test Items

No.	Test Item	Test Condition
1	High Temperature Operation	Ts = +70°C, 96 hours
2	ILow Temperature Operation	Ts = -20°C, 96 hours
3	High Temperature Storage	Ta = +80℃, 96 hours
4	Low Temperature Storage	Ta = -30°C, 96 hours
5	Storage at HighTemperature and Humidity	Ta = +60℃, 90% RH,48hours

NOTE:

- 1. All judgement of display are performed after temperature of panel return to room temperature.
- 2. Display function should be no change under normal operating condition.
- 3. Under no condensation of dew.
- 4. CPT only guarantee the above 5 test items, and without guarantee the others.

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9. Handling Precautions

- 9.1 Handling Precautions
 - 9.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
 - 9.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 9.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 9.1.4 If the display surface is contaminated, breathe on the surface and gently wipe it with a softdry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 9.1.5 Do not attempt to disassemble the LCD.
- 9.1.6 If the logic circuit power is off, do not apply the input signals.
- 9.1.7 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - 9.1.7.1 Be sure to ground the body when handling the LCD.
 - 9.1.7.2 Tools required for assembly, such as soldering irons, must be properly ground.
 - 9.1.7.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

9.2 Storage precautions

- 9.2.1 When storing the LCD, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 9.2.2 The LCD should be stored under the storage temperature range. If the LCD will be stored for a long time, the recommend condition is:

Temperature : 0° C $\sim 40^{\circ}$ C Relatively humidity: $\leq 80\%$

- 9.2.3 The LCD should be stored in the room without acid, alkali and harmfullgas.
- 9.3 Transportation Precautions:
 - The LCD should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

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10. QC

10.1 Purpose

To ensure the stability of our product and standardize our inspection

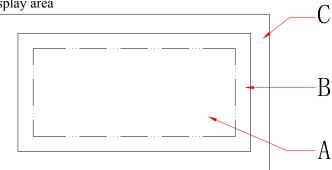
10.2 Application Range

This standard is applied to all 4.3 inch and smaller sized LCM product from Elsun Technology Co.Ltd

10.3 Definition of inspection area

C area: The area covered after installation

B area: visible area A area: display area

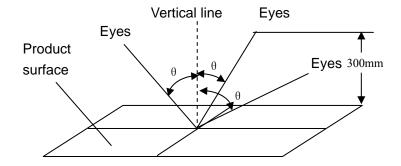


10.4 The environmental condition of inspection

Lighting conditions should be $20 \sim 40$ W fluorescent lamp (illumination at 1000 ± 200 lux)

Test ambient temperature should be 23 ± 5 °C, humidity at 50 ± 20% RH

The tested products should be placed 300mm away from the examiner's eye, and 30 degrees in the vertical direction observed within the region



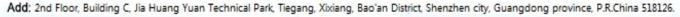
10.5 Identification

- 10.5.1 Bright dot: dots appearing bright and unchanged in size when the LCD panel is under black pattern.
- 10.5.2 Dark dot: dots appearing dark and unchanged size when the LCD panel is under RGB picture.

10.6 Inspection items and criteria

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10.6.1 Serious defect

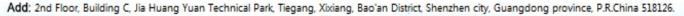
No	inspection item	inspection criteria	
10.6.1.1	function failure	 Non-display not allowed Line missing not allowed Invalid touch and drift not allowed (if need) 	main defect
10.6.1.2	break	broken display not allowed	main defect
10.6.1.3	dimension Dimension tolerance out of specified in the drawing not allowed.		main defect

10.6.2 Appearance defect

No	Inspection item	inspection criteria				defect grade
	Dot defect black dot, white dot, dirt on surface, stain, bubble	1. dot defect identi $\Phi = \frac{(\lambda + 1)^2}{2}$				
10.6.2.1		2. inspection criter Area and	-			Minor defect
		quantit dimension(mm) $\Phi \leqslant 0.15$	quantity allowed			
			y A area B area	B area	C area	
			ignore			
		0.15< Φ ≤0.20	2 (spacing>=10mm)		ignore	
		0.20< Φ ≤0.3	1			
		$\Phi > 0.3$	0			
					1	

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No	Inspection item	inspection criteria				defect grade	
		1. identification of line dimension L: length W: width					
		2. inspection cr	iteria				
10.6.2.2	line defect visible	dimensio	on(mm)	quantity	allowed pcs)	Minor defect	
	black/white line	L (length)	W (width)		area	1	
				A area	B area	C area	
		ignore L≤3.0	W≤0.03 0.03 <w≤ 0.05</w≤ 	ignore 2			
		L≤3.0	0.05 <w≤ 0.08</w≤ 	ignore 1			
		W>0. 08 count according to dot defect					
		1-If the scratch to 10.6.2.2 2-If the scratch non-working sta	ch is visible	at spec	ial ange	el or at	
		dimension	n (mm)	Quantity allowed			
		L (length)	W (width)	area			
10.6.2.3	scratch	L (length)		A	В	C	Minor defect
		ignore	W≤0.03	ignore			
		5.0 <l≤10. 0<="" td=""><td>0.03<w≤ 0.05</w≤ </td><td colspan="2">2</td><td rowspan="2"></td></l≤10.>	0.03 <w≤ 0.05</w≤ 	2			
		L≤5. 0	0.05 <w≤ 0.08</w≤ 	ignore 1			
			W>0.08	Not a	llowed		

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No	Inspection item	inspection criteria	defect grad
10.6.2.4	Glass defect	1. broken angle X 不计 Y≤2.0mm or X≤2.0mm Y 不计 Meanwhile Z <t 2.="" broken="" ignore="" ignore<="" meanwhile="" other="" part="" td="" x≤5.0mm="" y≤0.8mm="" z≤t=""><td>Minor defe</td></t>	Minor defe
10.6.2.5	Newton ring	1.regular Newton ring ① Newton ring area>1/3 T/P area; not acceptable. ② Newton ring area≤1/3 T/P area and doesn't affect the display result and no line distortion; acceptable 2. Non-regular Newton ring ① Newton ring area≥1/2 T/P area, or no matter how big as long as it affects the display result: not acceptable Newton ring area≤1/2 T/P area, and doesn't affect the	Minor defe

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NO	Inspection item	inspection criteria	defect grade
		 copper foil off, warping, crack and oxidation are not allowed FPC crack, break, serious scratch and crease are not allowed 	main defect
10.6.2.6	FPC	 if no special requirements, no release paper on double-sided adhesive FPC is not allowed. Slight creases and scratches not exposed from the copper foil and with no affect to appearance and function are allowed. if no special requirements, no insulating tape at welding part on backlight and touch-screen is not allowed. Parts off, breakage and deform are not allowed. print on the surface should be clear and correct. 	Minor defect
10.6.2.7	basic appearance requirements	 clean appearance, no dirt, fingerprints and other traces. ITO circuit on COG coating area should not be exposed. Rust, sever scratch, deformation, obvious burrs and color dirt are not allowed. Mis-assembly, part missing are not allowed. Bubble caused by mis-pasted polaroid refers to 10.6.2.1 For watermark, the criteria is upon agreed by both parties. 	Minor defect

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10.6.3 electric defect

No	Inspection item	inspection criteria	defect grade
10.6.3.1	picture defect	Non-display, more or less image and display defect are not allowed.	main defect
10.6.3.2	bright/dark line	Not allowed.	main defect
10.6.3.3	display dot defect	 one dot is acceptable. Under bright status, 2 dark dots with more than 5mm distance is allowed. Totally 2 bright or dark dots are acceptable. The other defect under bright status refers to 10.6.2.1 Note: Electric bright/dark dot means one pixel; less than 1/2 of 1 pixel can be ignored. 	Minor defect
10.6.3.4	connected dot/line defect	 Two continuous defect pixel connected dots are not allowed. Line defect refers to 10.6.2.2 	Minor defect
10.6.3.5	wrong view direction	Wrong view directions, such as opposite view angle, are not allowed.	main defect
10.6.3.6	back light defect	 Backlight off are not allowed. Uneven light, dead light, flicker light, dark angle, light leakage are not allowed. Brightness should comply with drawing 	main defect

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