

DLC Display Co., Limited

德爾西顯示器有限公司



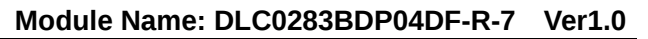
MODEL No: DLC0283BDP04DF-R-7

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Date	Revision No.	Summary
2026-08-13	1.0	Rev 1.0 was issued

1. Scope

This data sheet is to introduce the specification of DLC0283BDP04DF-R-7 active matrix TFT module. It is composed of a color TFT-LCD panel, driver IC, FPC, RTP and a backlight unit. The 2.8" display area contains 240(RGB) x 320 pixels.

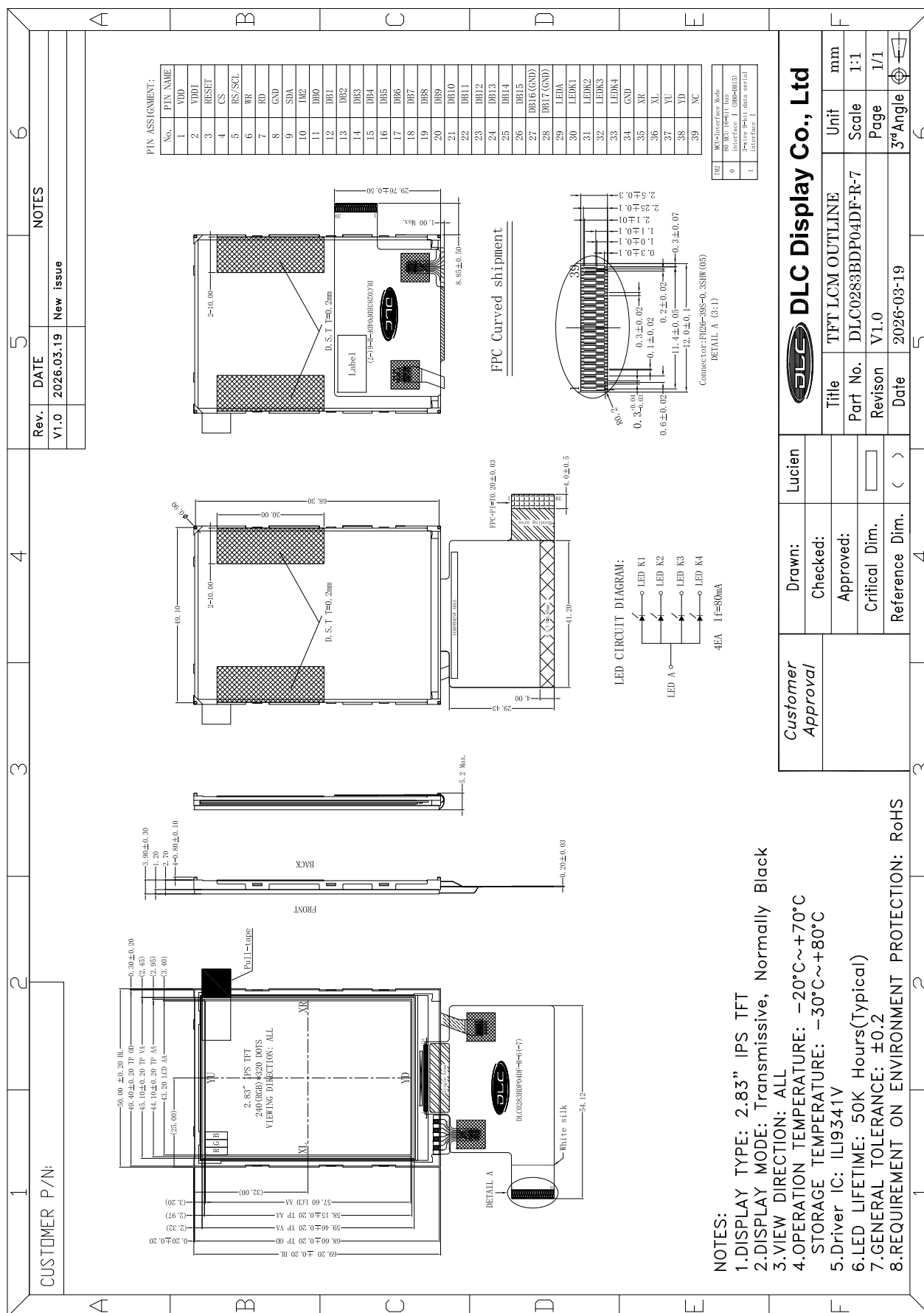
2. Application

Digital equipments which need color display, mobile navigator/video systems.

3. General Information

Item	Contents	Unit
Size	2.8	inch
Resolution	240(RGB) x 320	/
Interface	SPI/MCU	/
Technology type	IPS TFT	/
Pixel pitch	0.18 x 0.18	mm
Pixel Configuration	R.G.B Vertical Stripe	
Outline Dimension (W x H x D)	50.00 x 69.20 x 3.90	mm
Active Area	43.20 x 57.60	mm
Display Mode	Transmissive, Normally Black	/
Backlight Type	LED	/
Driver IC	ILI9341V	/
Viewing Direction	ALL	/
Weight	18.4 (Typical)	g

4. Outline Drawing



5. Interface signals

No	Symbol	I/O	Description	Remarks
1	VDD	P	Power supply voltage for logic circuit (2.5V~3.3V)	
2	VDDI	P	Power supply voltage for analog circuit (1.65V~3.3V)	
3	RESET	I	This signal will reset the device and it must be applied to properly initialize the chip	
4	CS	I	Chip selection pin. Low-active	
5	RS/SCL	I	(RS): This pin is used to select "Data or Command" in the parallel interface. When RS = 1, data is selected. When RS = 0, command is selected. (SCL): This pin is used as the serial interface clock in 3-wire 9-bit/4-wire 8-bit serial data interface.	
6	WR	I	Serves as a write signal and writes data at the rising edge	
7	RD	I	Serves as a read signal and MCU read data at the rising edge	
8	GND	P	Ground	
9	SDA	I	Power supply for analog circuit	
10	IM2	I	Select the MCU interface mode	Note1
11	DB0	I/O	Data bus	
12	DB1	I/O	Data bus	
13	DB2	I/O	Data bus	
14	DB3	I/O	Data bus	
15	DB4	I/O	Data bus	
16	DB5	I/O	Data bus	
17	DB6	I/O	Data bus	
18	DB7	I/O	Data bus	
19	DB8	I/O	Data bus	
20	DB9	I/O	Data bus	
21	DB10	I/O	Data bus	
22	DB11	I/O	Data bus	
23	DB12	I/O	Data bus	
24	DB13	I/O	Data bus	
25	DB14	I/O	Data bus	
26	DB15	I/O	Data bus	
27	DB16(GND)	I/O	Data bus (Ground)	
28	DB17(GND)	I/O	Data bus (Ground)	
29	LEDA	I	LED backlight anode	

30	LEDK1	I	LED backlight cathode	
31	LEDK2	I	LED backlight cathode	
32	LEDK3	I	LED backlight cathode	
33	LEDK4	I	LED backlight cathode	
34	GND	P	Ground	
35	XR	I	Touch panel control pin, Right	
36	XL	I	Touch panel control pin, Left	
37	YU	I	Touch panel control pin, Up	
38	YD	I	Touch panel control pin, Down	
39	NC	-	No connection	

Note1:

IM2	MCU Interface Mode
0	80 MCU 16-bit bus interface I (DB0~DB15)
1	3-wire 9-bit data serial interface I

6. Absolute maximum Ratings

6.1. Electrical Absolute max. ratings

Parameter	Symbol	MIN	MAX	Unit	Remark
Power Supply Voltage	VDD	-0.3	4.6	V	
	VDDI	-0.3	4.6	V	

6.2. Environment Conditions

Item	Symbol	MIN	MAX	Unit	Remark
Operating Temperature	TOPR	-20	70	°C	
Storage Temperature	TSTG	-30	80	°C	

7. Electrical Specifications

7.1 Electrical characteristics

GND=0V, Ta=25°C

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Power Supply Voltage	VDD	2.5	2.8	3.3	V	
	VDDI	1.65	2.8	3.3	V	
Input Signal Voltage	VIL	GND	--	0.3*VDDI	V	
	VIH	0.7*VDDI	--	VDDI	V	
LCD Drive Power current	ILCD	--	15	--	mA	VDD=2.8V

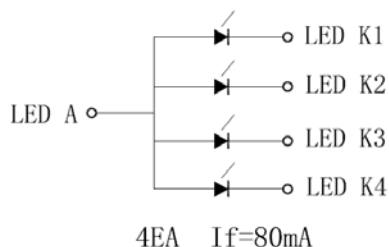
7.2 LED Backlight

Ta=25°C

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Forward Current	IF	-	80	-	mA	
Forward Voltage	VF	2.7	-	3.3	V	
LED Life time	-	-	50,000	-	Hrs	Note

Note: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and IL=80mA. The LED lifetime could be decreased if operating IL is larger than 80mA.

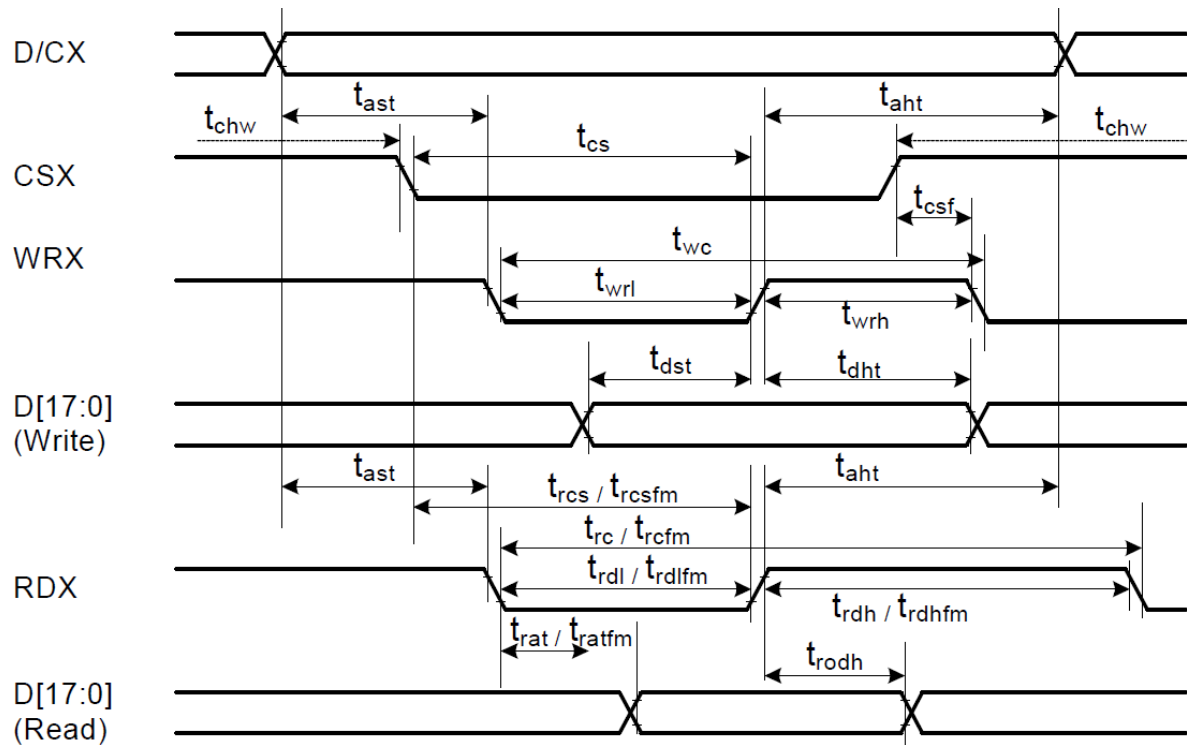
LED CIRCUIT DIAGRAM:



8. Command/AC Timing

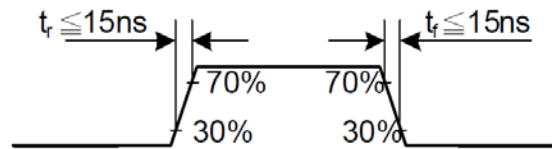
8.1 AC Characteristics

8.1.1 Display Parallel 18/16/9/8-bit Interface Timing Characteristics (8080-I system)

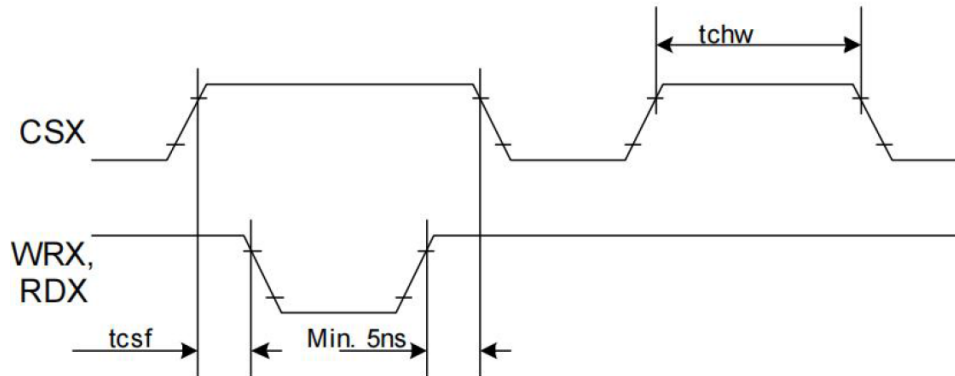


Signal	Symbol	Parameter	min	max	Unit	Description
DCX	tast	Address setup time	0	-	ns	
	taht	Address hold time (Write/Read)	0	-	ns	
CSX	tchw	CSX "H" pulse width	0	-	ns	
	tcs	Chip Select setup time (Write)	15	-	ns	
	trcs	Chip Select setup time (Read ID)	45	-	ns	
	trcsfm	Chip Select setup time (Read FM)	355	-	ns	
WRX	tcsf	Chip Select Wait time (Write/Read)	10	-	ns	
	twc	Write cycle	66	-	ns	
	twrl	Write Control pulse L duration	15	-	ns	
RDX (FM)	twrh	Write Control pulse H duration	15	-	ns	
	trcfm	Read Cycle (FM)	450	-	ns	
	trdhfm	Read Control H duration (FM)	90	-	ns	
	trdlfm	Read Control L duration (FM)	355	-	ns	
RDX (ID)	trc	Read cycle (ID)	160	-	ns	
	trdh	Read Control pulse H duration	90	-	ns	
	trdl	Read Control pulse L duration	45	-	ns	
D[17:0], D[15:0], D[8:0], D[7:0]	tdst	Write data setup time	10	-	ns	
	tdht	Write data hold time	10	-	ns	
	trat	Read access time	-	40	ns	For maximum CL=30pF For minimum CL=8pF
	tratfm	Read access time	-	340	ns	
	trod	Read output disable time	20	80	ns	

Note: Ta = -30 to 70 °C, VDDI=1.65V to 3.3V, VCI=2.5V to 3.3V, VSS=0V

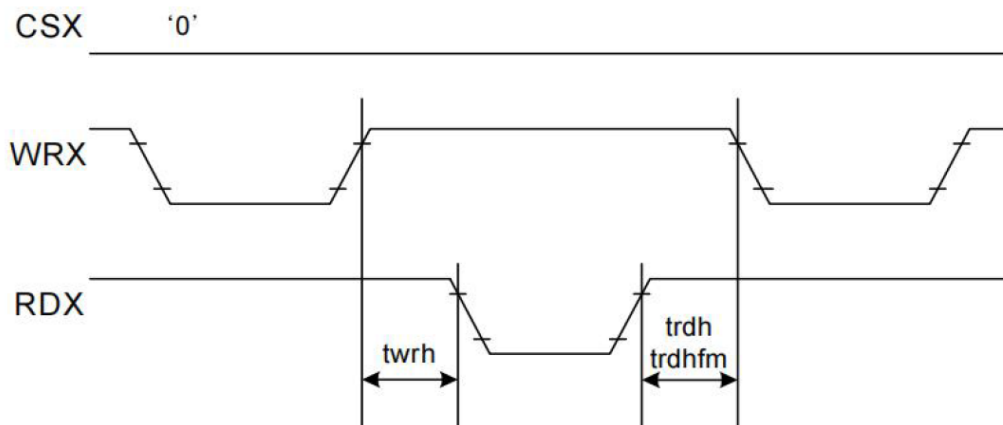


CSX Timings:



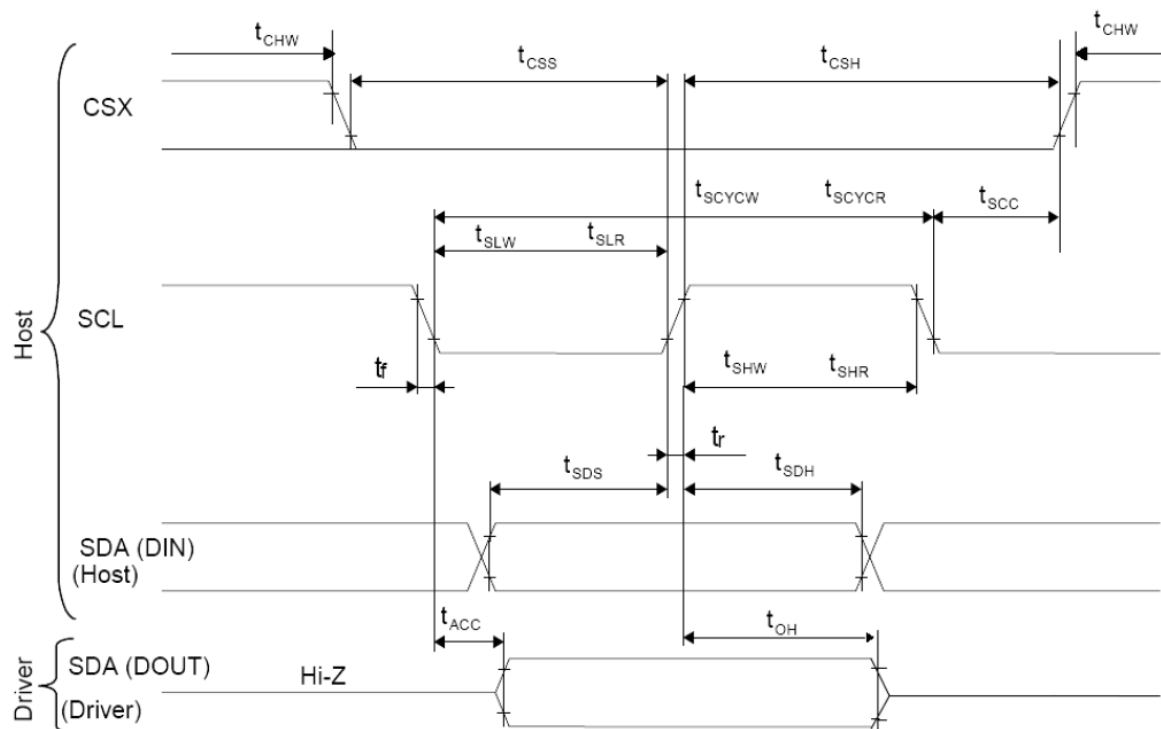
Note: Logic high and low levels are specified as 30% and 70% of VDDI for input signals.

Write to read or read to write timings:



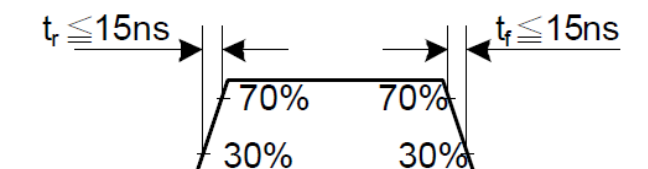
Note: Logic high and low levels are specified as 30% and 70% of VDDI for input signals.

8.1.2 Display Serial Interface Timing Characteristics (3-line SPI system)

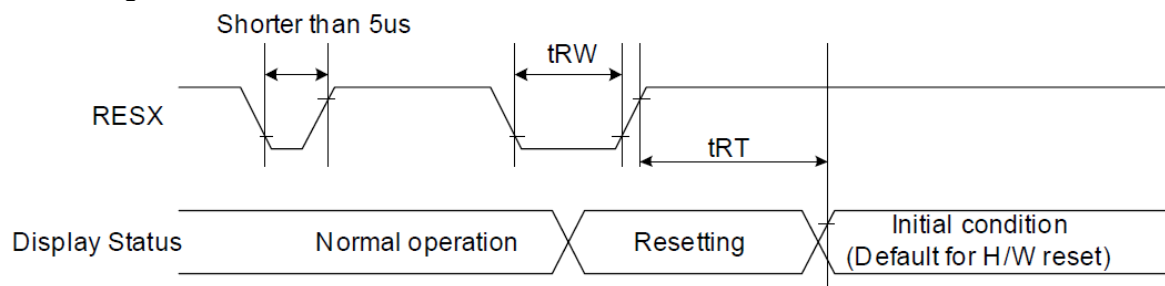


Signal	Symbol	Parameter	min	max	Unit	Description
SCL	tscycw	Serial Clock Cycle (Write)	100	-	ns	
	tshw	SCL "H" Pulse Width (Write)	40	-	ns	
	tslw	SCL "L" Pulse Width (Write)	40	-	ns	
	tscycr	Serial Clock Cycle (Read)	150	-	ns	
	tshr	SCL "H" Pulse Width (Read)	60	-	ns	
	tslr	SCL "L" Pulse Width (Read)	60	-	ns	
SDA / SDI (Input)	tsds	Data setup time (Write)	30	-	ns	
	tsdh	Data hold time (Write)	30	-	ns	
SDA / SDO (Output)	tacc	Access time (Read)	10	-	ns	
	toh	Output disable time (Read)	10	50	ns	
CSX	tsc	SCL-CSX	20	-	ns	
	tchw	CSX "H" Pulse Width	40	-	ns	
	tcss	CSX-SCL Time	60	-	ns	
	tcsh		65	-	ns	

Note: Ta = 25 °C, VDDI=1.65V to 3.3V, VCI=2.5V to 3.3V, AGND=VSS=0V



8.2 Reset Timing



Signal	Symbol	Parameter	Min	Max	Unit
RESX	tRW	Reset pulse duration	10		uS
	tRT	Reset cancel		5 (note 1,5)	mS
				120 (note 1,6,7)	mS

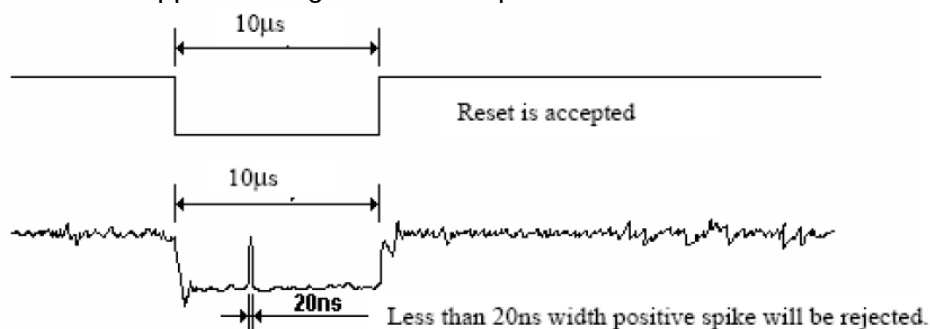
Note 1: The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NV memory to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.

Note 2: Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 10us	Reset
Between 5us and 10us	Reset starts

Note 3: During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In -mode.) And then return to Default condition for Hardware Reset.

Note 4: Spike Rejection also applies during a valid reset pulse as shown below:



Note 5: When Reset applied during Sleep In Mode.

Note 6: When Reset applied during Sleep Out Mode.

Note 7: It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

9. Optical Specification

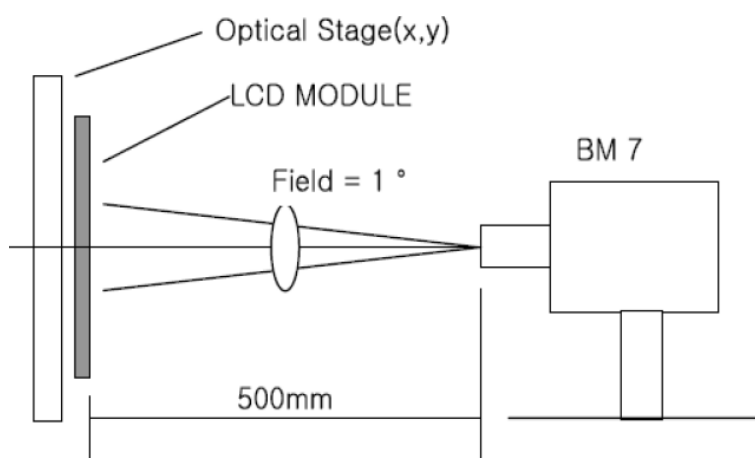
Ta=25℃

Item		Symbol	Condition	Min	Typ.	Max.	Unit	Remark
Contrast Ratio		CR	θ=0°	1000	1200	-		Note1 Note2
Response Time		Ton/ Toff	25℃	-	30	35	ms	Note1 Note3
View Angles		ΘT	CR≥10	75	80	-	Degree	Note 4
		ΘB		75	80	-		
		ΘL		75	80	-		
		ΘR		75	80	-		
Chromaticity	White	x	Brightness is on	Typ-0.05	0.3273	Typ+0.05		Note5, Note1
		y			0.3620			
	Red	x			0.6797			
		y			0.3225			
	Green	x			0.3611			
		y			0.6126			
	Blue	x			0.0818			
		y			0.1158			
Luminance		L		-	350	-	cd/m²	Note1 Note6
Uniformity		U		80	-	-	%	Note1 Note7

Note 1: Definition of optical measurement system.

Temperature = 25℃ (±3℃)

LED back-light: ON, Environment brightness < 150 lx

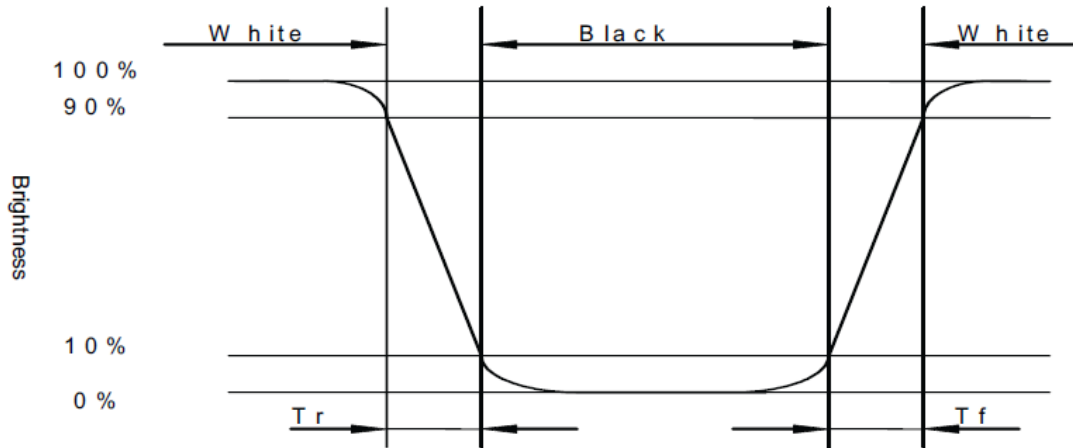


Note 2: Contrast ratio is defined as follow:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

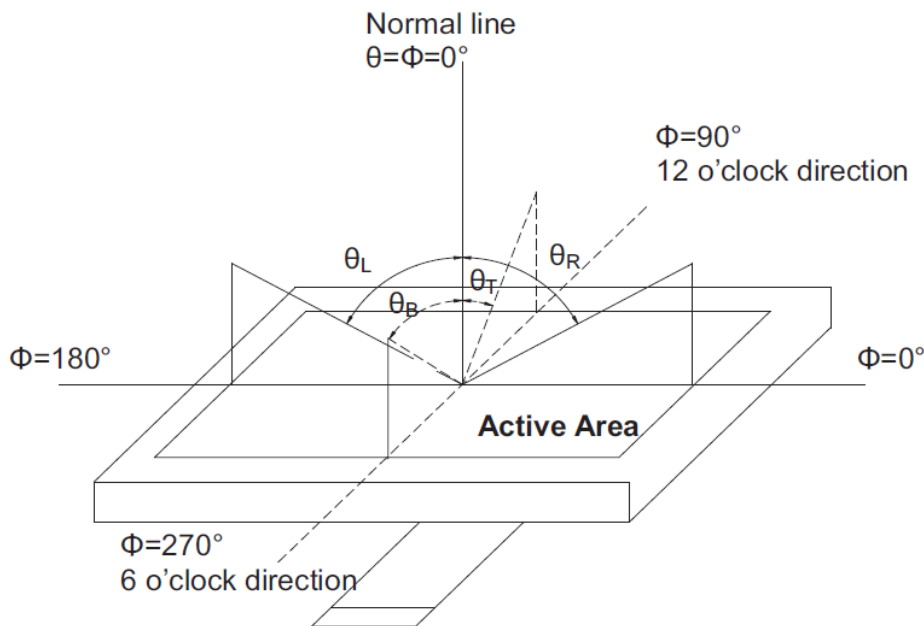
Note 3: Response time is defined as follow:

Response time is the time required for the display to transition from black to white (Rise Time, T_r) and from white to black (Decay Time, T_f).



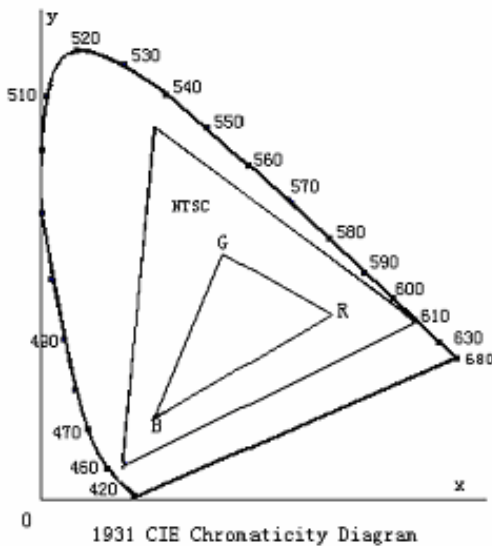
Note 4: Viewing angle range is defined as follow:

Viewing angle is measured at the center point of the LCD.



Note 5: Color chromaticity is defined as follow: (CIE1931)

Color coordinates measured at center point of LCD.



$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

Note 6: Luminance is defined as follow:

Luminance is defined as the brightness of all pixels “White” at the center of display area on optimum contrast.

Note 7: Luminance Uniformity is defined as follow:

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

$$\text{Uniformity}(U) = \frac{\text{Minimum Luminance(brightness) in 9 points}}{\text{Maximum Luminance(brightness) in 9 points}}$$

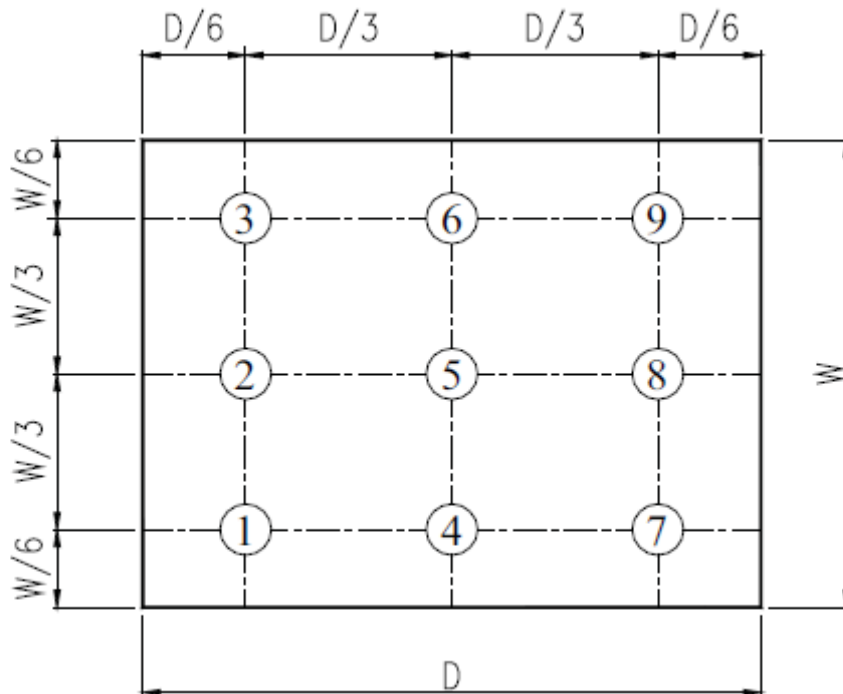


Fig. 2 Definition of uniformity

10. Environmental / Reliability Tests

No	Test Item	Condition	Judgment criteria
1	High Temp Operation	Ts=+70℃, 120hrs	Per table in below
2	Low Temp Operation	Ta=-20℃, 120hrs	Per table in below
3	High Temp Storage	Ta=+80℃, 120hrs	Per table in below
4	Low Temp Storage	Ta=-30℃, 120hrs	Per table in below
5	High Temp & High Humidity Storage	Ta=+40℃, 90% RH, 120 hours	Per table in below (polarizer discoloration is excluded)
6	Thermal Shock (Non-operation)	-30℃ 30 min~+70℃ 30 min, Change time:5min, 10 Cycles	Per table in below
7	ESD (Operation)	C=150pF, R=330Ω, 5points/panel Air: ±8KV, 5times; Contact: ±4KV, 5 times;	Per table in below
8	Vibration (Non-operation)	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z.	Per table in below
9	Package Drop Test	Height:80 cm, 1 corner, 3 edges, 6 surfaces	Per table in below

INSPECTION	CRITERION(after test)
Appearance	No Crack on the FPC, on the LCD Panel
Alignment of LCD Panel	No Bubbles in the LCD Panel No other Defects of Alignment in Active area
Electrical current	Within device specifications
Function / Display	No Broken Circuit, No Short Circuit or No Black line No Other Defects of Display

11. Precautions for Use of LCD Modules

11.1 Safety

The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

11.2 Handling

A. The LCD and touch panel is made of plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.

B. Do not handle the product by holding the flexible pattern portion in order to assure the reliability

C. Transparency is an important factor for the touch panel. Please wear clear finger sacks, gloves and mask to protect the touch panel from finger print or stain and also hold the portion outside the view area when handling the touch panel.

D. Provide a space so that the panel does not come into contact with other components.

E. To protect the product from external force, put a covering lens (acrylic board or similar board) and keep an appropriate gap between them.

F. Transparent electrodes may be disconnected if the panel is used under environmental conditions where dew condensation occurs.

G. Property of semiconductor devices may be affected when they are exposed to light, possibly resulting in IC malfunctions.

H. To prevent such IC malfunctions, your design and mounting layout shall be done in the way that the IC is not exposed to light in actual use.

11.3 Static Electricity

A. Ground soldering iron tips, tools and testers when they are in operation.

B. Ground your body when handling the products.

C. Power on the LCD module before applying the voltage to the input terminals.

D. Do not apply voltage which exceeds the absolute maximum rating.

E. Store the products in an anti-electrostatic bag or container.

11.4 Storage

A. Store the products in a dark place at $+25^{\circ}\text{C} \pm 10^{\circ}\text{C}$ with low humidity (40% RH to 60% RH).

Don't expose to sunlight or fluorescent light.

B. Storage in a clean environment, free from dust, active gas, and solvent.

11.5 Cleaning

A. Do not wipe the touch panel with dry cloth, as it may cause scratch.

B. Wipe off the stain on the product by using soft cloth moistened with ethanol. Do not allow ethanol to get in between the upper film and the bottom glass. It may cause peeling issue or defective operation. Do not use any organic solvent or detergent other than ethanol.

11.6 Cautions for installing and assembling

A. Bezel edge must be positioned in the area between the Active area and View area. The bezel may press the touch screen and cause activation if the edge touches the active area. A gap of approximately 0.5mm is needed between the bezel and the top electrode. It may cause unexpected activation if the gap is too narrow. There is a tolerance of 0.2 to 0.3mm for the outside dimensions of the touch panel and tail. A gap must be made to absorb the tolerance in the case and connector.

B. In order to make the display assembly stable and firm, DLC recommends to design some supporting at the display backside, especially for the display with tape-attached touch panel, such supporting is important and essential, or else, the display may drop-off from front after some period of time.

C. Do not display the fixed pattern for a long time because it may develop image sticking due to the LCD structure. If the screen is displayed with fixed pattern, use a screen saver.

